

LIQUID CRYSTAL DISPLAY MODULE

Product Specification

DENSITRON	STANDARD LCD MODULE	
PRODUCT NUMBER	LWM24064A-SERIES	
DEFINITION	Display 240*64 dots	Date 19/04/04

INTERNAL APPROVALS				
Quality Mgr	Product Mgr	Project Leader	Mech. Eng	Electr. Eng
Date:	Date:	Date:	Date:	Date:

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REVISION RECORD

Rev.	Date	Page	Chapt.	Comment	ECR no.
1	19/04/2004			Initial Specification	

1 PART NUMBERING SYSTEM

LWM 24064A – xx – x xx / ()
Densitron mono module ① ② ③ ④ ⑤ ⑥

① Resolution dots width x high

② Model serials number

③ Display mode and backlight type :

A = Reflective (without backlight) =

B* = Transflective positive LEDS array type backlight (BG, BA, BW, BR, BT ...)

B*(E) = Transflective LEDS edge type backlight (BGE, BAE, BWE, BR...)

B = Transflective CFL

C* = Transflective positive EL backlight (CB, CW)

D* = Transmissive negative EL backlight (DB, DW)

E = CFL

E* = Transmissive negative LEDS array type backlight (EG, EA, EW, ER...)

E*(E) = Transmissive negative LEDS edge type backlight (EGE, EAE, EWE, ERE...)

(color LED or EL backlight = G/ yellow-green, A/ amber, W/ white, R/ red, B/blue ,T/ tricolour)*

④ Temperature range and power supply

D = Standard temperature range; negative supply voltage required (0°C~+50°C)

S = Standard temperature range; on board negative voltage generator (0°C~+50°C)

H= Wide temperature range; negative supply voltage required (-20°C ~+70°C)

W= Wide temperature range; on board negative voltage generator (-20°C ~+70°C)

⑤ Fluid type and compensation circuit option

NY = STN yellow-green glass, without temperature compensation circuit

CY = STN yellow green glass, with temperature compensation circuit on board

NG = STN gray glass without temperature compensation circuit

CG = STN gray glass with temperature compensation circuit

NB = STN blue glass, without temperature compensation circuit

CB = STN blue glass with temperature compensation circuit

NF = FSTN black and white glass without temperature compensation circuit

CF = FSTN black and white glass with temperature circuit on board

⑥ Special code for customized features or additional features

12 = it is mentioned if top view angle is needed

HL = High Luminosity for White LED Backlight if needed

Other code = Please refer to our commercial office

Remarks and definitions :

1°) Display mode and backlight type :

- Reflective polarizer, no backlight, usable only in good ambient light conditions.
- Transflective polarizer uses a background backlight and a mirror reflector, usable in all lighting conditions.
- Transmissive polarizer needs the backlight switched on continuously, usable mostly in low ambient light conditions
- EL (electro-luminescent), uniform brightness, short life time (8000 hours max), needs EL inverter, low current consumption, low thickness.
- LED (light emitting diode), uniform brightness, long lifetime (100 000 hours), doesn't need inverter, reliable in vibration and shock environment, different colors available.

Array version is a direct lighting type, available in standard form, uniform and good brightness on all the active area.

Edge version is an edge lighting type providing a low consumption backlight, has a low thickness and brightness.

- Positive mode has dark pixels on a light background
- Negative mode has light pixels in a dark background and is normally used only with a transmissive polarizer as it needs a backlight on to be visible.

2°) Fluid type :

- STN yellow green or grey LCD has a yellow green or grey background with dark blue pixels and offers a good contrast.
- FSTN LCD is to produce very high contrast with black and white pixels using a film polarizer

2 MAIN FEATURES

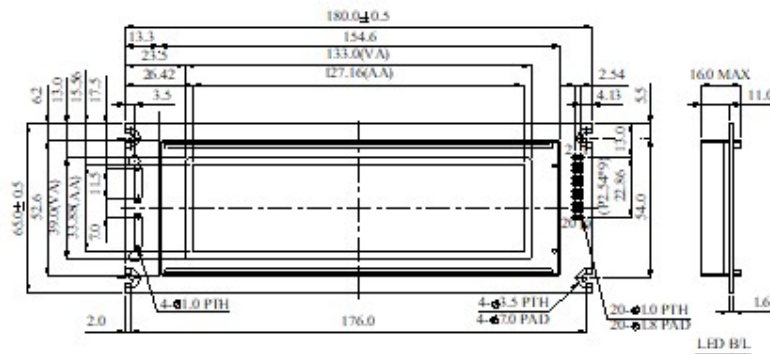
ITEM	CONTENTS
Display Format	240 * 64 dos
Overall Dimensions	180.0 x 65.0 x 16.0(MAX)
Viewing Area	133.0 x 39.0
LCD type	STN / FSTN
Mode	Available in Reflective / Transflective / Transmissive
Viewing Angle	6 o'clock
Duty ratio	1/64
Driver IC	T6963C or equivalent
Backlight type	None / LED
Backlight colour	Yellow Green / White / other on request
DC/DC converter	None or Included
Operating temperature	-20°/+70°C
Storage temperature	-30°/+80°C

3 MECHANICAL SPECIFICATION

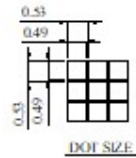
3.1 MECHANICAL CHARACTERISTICS

ITEM	CHARACTERISTIC	UNIT
Display Format	240 * 64 dots	
Overall Dimensions	180.0 x 65.0 x 16.0(MAX)	mm
Viewing Area	133.0 x 39.0	mm
Active Area	127.16 x 33.88	mm
Dot Size	(L)0.48×(W)0.48	mm
Dot Pitch	(L)0.53×(W)0.53	mm
IC Controller/Driver	T6963C	

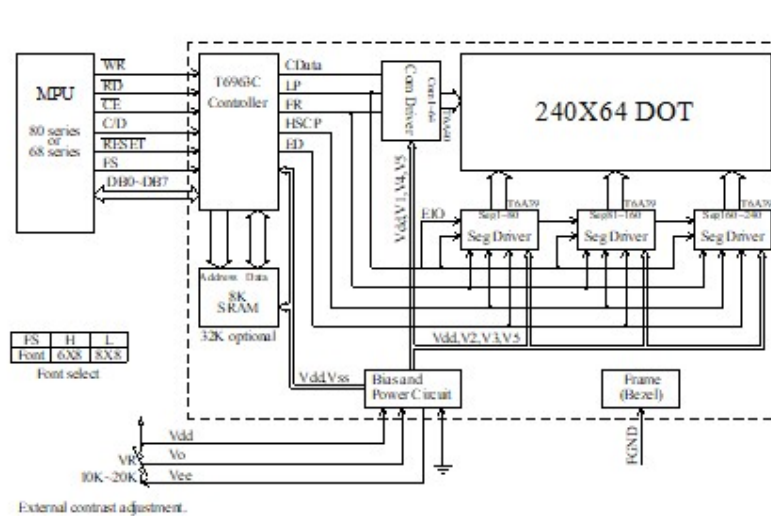
3.2 MECHANICAL DRAWING



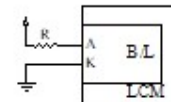
PIN #	SYMBOL
1	FGND
2	Vss
3	Vdd
4	Vo
5	WR
6	RD
7	CE
8	C/D
9	Vee
10	RESET
11	DB0
12	DB1
13	DB2
14	DB3
15	DB4
16	DB5
17	DB6
18	DB7
19	FS
20	NC



The non-specified tolerance of dimension is $\pm 0.3\text{mm}$.



LED B/L drive directly from AK.



4 ELECTRICAL SPECIFICATION

4.1 ABSOLUTE MAXIMUM RATINGS

VSS = 0 V, Ta = 25 °C

Item	Symbol	Min	Typ	Max	Unit	Note
Power Supply Voltage	$V_{DD}-V_{SS}$	-0,3	-	+7,0	V	
Power Supply for LCD	$V_{DD}-V_0$	0	-	15	V	
Input Voltage	V_{in}	V_{SS}	-	V_{DD}	V	
Operating Temperature	Top	0 -20	-	+50 +70	°C	Note 1
Storage Temperature	Tst	-20 -30	-	+70 +80	°C	Note 2
Static Electricity	Be sure that you are grounded when handling displays.					

Note 1: Background colour changes slightly depending on ambient temperature. This phenomenon is reversible. Ta ≤ 70 °C: 75% RH max

Note 2: Ta ≤ 80 °C: 75% RH max

4.2 ELECTRICAL CHARACTERISTICS

VSS = 0 V, Ta = 25 °C

Item	Symbol	Condition	Min	Typ	Max	Unit
Power Supply for Logic	$V_{DD}-V_{SS}$	Ta = 25 °C	4,75	5,0	5,25	V
Input Voltage	V_{IL}	Ta = 25 °C	0	-	0,8	V
	V_{IH}	Ta = 25 °C	2,2	-	V_{DD}	V
Output Voltage	V_{OL}	Ta = 25 °C	0	-	0,4	V
	V_{OH}	Ta = 25 °C	2,4	-	V_{DD}	V
LCD Module Driving Voltage	$V_{DD}-V_0$	Ta = -20 °C	-	-	13	V
		Ta = 25 °C	-	12	-	V
		Ta = 70 °C	11	-	-	V
Current Consumption	* I_{DD}	$V_{DD} = 5V$ $V_{LCD} = 12V$	-	16	-	mA

* I_{DD} measurement condition is for all pattern ON

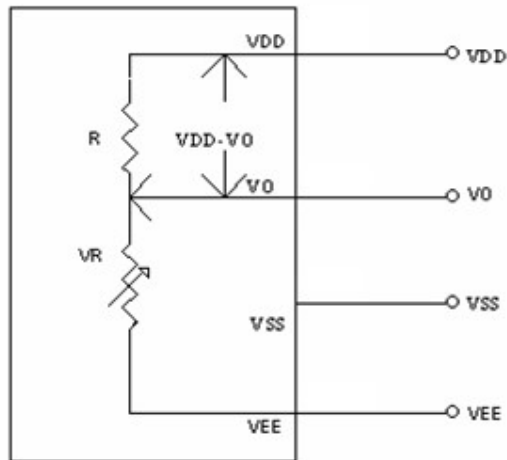
4.3 INTERFACE PIN ASSIGNMENT

Pin No.	Symbol	Level	Description
1	FG	—	Frame ground (Connected to bezel)
2	V _{SS}	—	GND
3	V _{DD}	—	Power supply (+5 V)
4	V _O	—	Power supply for LCD driver
5	/WR	L	Data write. Write data into T6963C when WR = L
6	/RD	L	Data read. Read data from T6963C when RD = L
7	/CE	L	L : Chip enable
8	C/D	H / L	WR=L , C/D=H : Command Write C/D=L: Data write RD=L , C/D=H : Status Read C/D=L: Data read
9	V _{EE}	—	Negative voltage –10V output
10	/RESET	H / L	H : Normal ; L : Initialize T6963C
11	DB0	H / L	Data bus line
12	DB1	H / L	Data bus line
13	DB2	H / L	Data bus line
14	DB3	H / L	Data bus line
15	DB4	H / L	Data bus line
16	DB5	H / L	Data bus line
17	DB6	H / L	Data bus line
18	DB7	H / L	Data bus line
19	FS	H / L	Pins for selection of font; H : 6 * 8 , L : 8 * 8
20	N.C	—	No connection



4.5 POWER SUPPLY CIRCUIT

Internal LCD V0 Adjustment



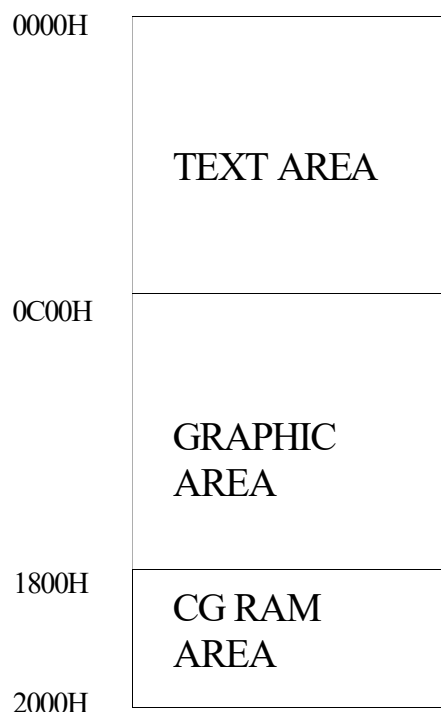
4.6 DISPLAY CONTROL INSTRUCTION

The LCD Module has built in a T6963C LSI controller, It has an 8-bit parallel data bus and control lines for writing or reading through an MPU interface, it has a 128-word character generator ROM (refer to Table 1.), which can control an external display RAM of up to 8K bytes. Allocation of text, graphics and external character generator RAM can be made easily and the display window can be moved freely within the allocated memory range.

•RAM Interface

The external RAM is used to store display data(text, graphic and external CG data). It can be freely allocated to the memory area(8 Kbyte max).

Recommend



Flowchart of communications with MPU

(1)Status Read

A status check must be performed before data is read or written. Status check

The Status of T6963C can be read from the data lines.

$\overline{\text{RD}}$	L
$\overline{\text{WR}}$	H
$\overline{\text{CE}}$	L
C/D	H
Do to D7	H

The T6963C status word format is as follows:

MSB

LSB

STA7	STA6	STA5	STA4	STA3	STA2	STA1	STA0
D7	D6	D5	D4	D3	D2	D1	D0

STA0	Check command execution capability	0:Disable 1:Enable
STA1	Check data read/write Capability	0:Disable 1:Enable
STA2	Check Auto mode data read capability	0:Disable 1:Enable
STA3	Check Auto mode data write capability	0:Disable 1:Enable
STA4	Not used	
STA5	Check controller operation capability	0:Disable 1:Enable
STA6	Error flag. Used for Screen Peek and Screen copy commands.	0:No error 1>Error
STA7	Check the blink condition	0:Disable off 1:Normal display

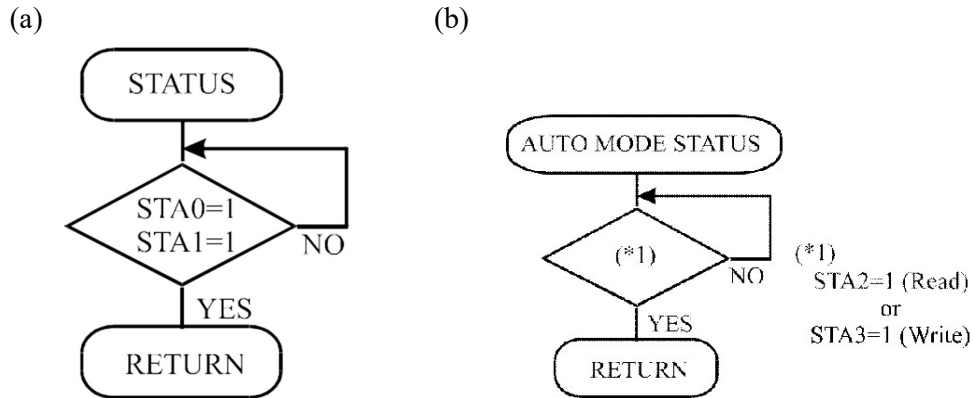
(Note 1) It is necessary to check STA0 and STA1 at the same time.

There is a possibility of erroneous operation due to a hardware interrupt.

(Note 2) For most modes STA0/STA1 are used as a status check.

(Note 3) STA2 and STA3 are valid in Auto mode; STA0 and STA1 are invalid.

Status Checking flow



(Note 4) When using the MSB=0 command, a Status Read must be performed.

If a status check is not carried out, the T6963C cannot operate normally, even after a delay time.

The hardware interrupt occurs during the address calculation period (at the end of each line).

If a MSB=0 command is sent to the T6963C during this period, the T6963C enters Wait status.

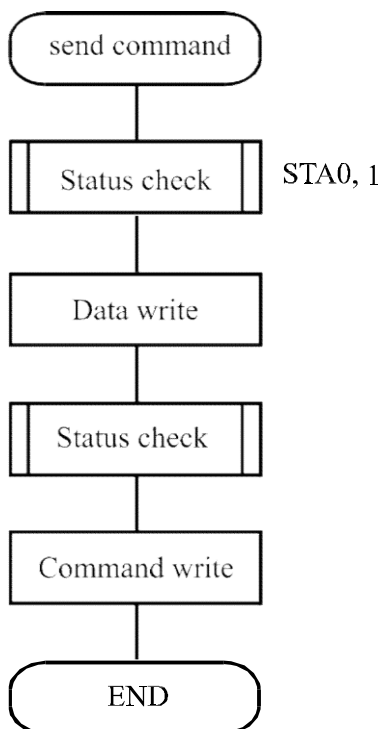
If a status check is not carried out in this state before the next command is sent, there is the possibility that the command or data data will not be received.

(2)Setting date

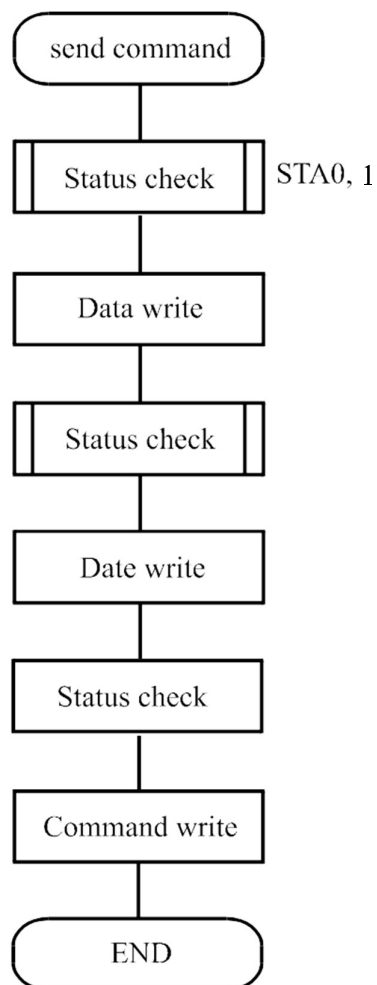
When using the T6963C, first set the data, then set the command.

Procedure for sending a command

(a)The case of 1 date



(b)The case of 2 data



(Note) When sending more than two data, the last datum (or last two data)is valid.

COMMAND DEFINITIONS

COMMAND	CODE	D1	D2	FUNCTION
REGISTERS SETTING	00100001	X address	Y address	Set Cursor Pointer
	00100010	Date	00H	Set Offset Register
	00100100	Low address	High address	Set Address Pointer
SET CONTROL WORD	01000000	Low address	High address	Set Text Home Address
	01000001	Columns	00H	Set Text Area
	01000010	Low address	High address	Set Graphic Home Address
	01000011	Columns	00H	Set Graphic Area
MODE SET	1000 × 000	—	—	OR mode
	1000 × 001	—	—	EXOR mode
	1000 × 011	—	—	AND mode
	1000 × 100	—	—	Text Attribute mode
	10000 × ×	—	—	Internal CG ROM mode
	10001 × ×	—	—	External CG RAM mode
DISPLAY MODE	10010000	—	—	Display off
	1001 × × 10	—	—	Cursor on, blink off
	10001 × × 11	—	—	Cursor on, blink on
	100101 × ×	—	—	Text on, graphic off
	100110 × ×	—	—	Text off, graphic on
	100111 × ×	—	—	Text on, graphic on
CURSOR PATTERN SELECT	10100000	—	—	1-line cursor
	10100001	—	—	2-line cursor
	10100010	—	—	3-line cursor
	10100011	—	—	4-line cursor
	10100100	—	—	5-line cursor
	10100101	—	—	6-line cursor
	10100110	—	—	7-line cursor
	10100111	—	—	8-line cursor
DATA AUTO READ/WRITE	10110000	—	—	Set Data Auto Write
	10110001	—	—	Set Data Auto Read
	10110010	—	—	Auto Reset
DATA READ/WRITE	11000000	Data	—	Data Write and Increment ADP
	11000001	—	—	Data Read and Increment ADP
	11000010	Data	—	Data Write and Decrement ADP
	11000011	—	—	Data Read and Decrement ADP
	11000100	Data	—	Data Write and Non-variable ADP
	11000101	—	—	Data Read and Non-variable ADP
SCREEN PEEK	11100000	—	—	Screen Peek
SCREEN COPY	11101000			Screen Copy

BIT SET/RESET	11110 × ×	—	—	
	11111 × ×	—	—	Bit Reset
	1111 × 000	—	—	Bit Set
	11111 × 001	—	—	Bit 0 (LSB)
				Bit 1
	11111 × 010	—	—	Bit 2
	11111 × 011	—	—	Bit 3
	11111 × 100	—	—	Bit 4
	11111 × 101	—	—	Bit 5
	11111 × 110	—	—	Bit 6
	11111 × 111	—	—	Bit 7 (MSB)

X: invalid

Setting registers

CODE	HEX.	FUNCTION	D1	D2
00100001	21H	SET CURSOR POINTER	X ADRS	Y ADRS
00100010	23H	SET OFFSET REGISTER	DATA	00H
00100100	24H	SET ADDRESS POINTER	LOW ADRS	HIGH ADRS

(1)Set Cursor Pointer

The position of the cursor is specified by X ADRS and Y ADRS. The cursor position can only be moved by this command. Data read/write from the MPU never changes the cursor pointer. X ADRS and Y ADRS are specified as follows.

X ADRS 00H to 4FH (lower 7 bits are valid)

Y ADRS 00H to 1FH (lower 5 bits are valid)

Single-Scan

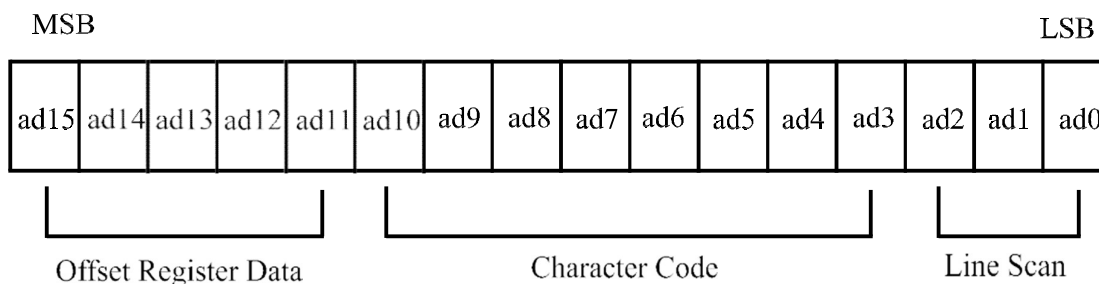
X ADRS 00 to 4FH

Y ADRS 00H to 0FH

(2)Set Offset Register

The offset register is used to determine the external character generator RAM area.

The T6963C has a 16-bit address bus as follows.



T6963C assign External character generator, when character code set 80H TO FFH in using internal character generator. Character code 00H to 80H assign External character generator, when External generator mode.

The senior five bits define the start address in external memory of the CG RAM area. The next eight bits represent the character code of the character. In internal CG ROM, character codes 00H to 7FH represent the predefined “internal” CG ROM characters, and codes 80H to FFH represent the user’s own “external” characters. In external CG ROM mode, all 256 codes from 00H to FFH can be used to represent the user’s own characters. The three least significant bits indicate one of the eight rows of eight dots that define the character’s shape.

The relationship between display RAM address and offset register

Offset register data	CG RAM hex. address (start to end)
00000	0000 to 07 FFH
00001	0800 to 0FFFH
00010	1000 to 17FFH
11100	E000 to E7FFH
11101	E800 to EFFFH
11110	F000 to F7FFH
11111	F800 to FFFFH

(Example 1)

Offset register	02H
Character code	80H
Character generator RAM start address	0001 0100 0000 0000
	1 4 0 0 H

								(address)	(data)
								1400H	00H
								1401H	1FH
								1402H	04H
								1403H	04H
								1404H	04H
								1405H	04H
								1406H	04H
								1407H	00H

(Example 2) The relationship between display RAM data and display characters

	(RAM DATA)	(Character)
AB γ DE ζ GHIJKLM	21H	A
⋮	22H	B
⋮	83H	γ
⋮	24H	D
⋮	25H	E
	86H	ζ

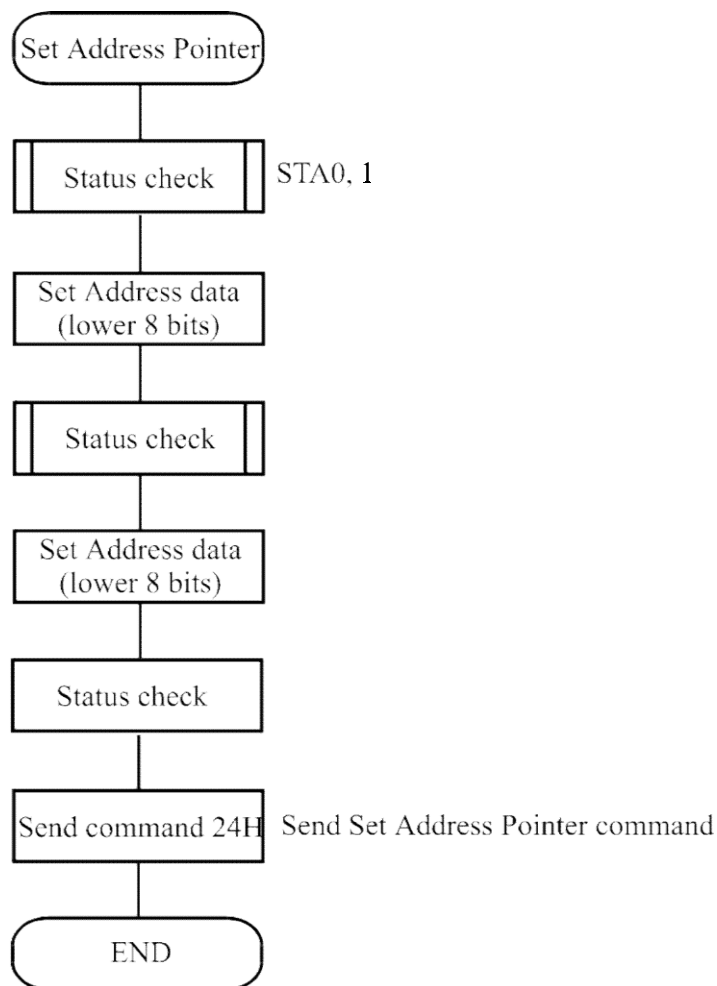
Display character

γ and ζ are displayed by character generator RAM.

(3)Set Address Pointer

The Set Address Pointer command is used to indicate the start address for writing to (or reading from) external RAM.

The Flowchart for Set Address Pointer command



Set Control Word

CODE	HEX.	FUNCTION	D1	D2
01000000	40H	Set Text Home Address	Low address	High address
01000001	41H	Set Text Area	Columns	00H
01000010	42H	Set Graphic Home Address	Low address	High address
01000011	43H	Set Graphic Area	Columns	00H

The home address and column size are defined by this command.

(1) Set Text Home Address

The starting address in the external display RAM for text display is defined by this command.

The text home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

TH		TH+CL
TH+TA		TH+TA+CL
(TH+TA)+TA		TH+2TA+CL
(TH+2TA)+TA		TH+3TA+CL
TH+(n-1)TA		TH+(n-1)TA+CL

TH: Text home address

TA: Text area number (columns)

CL: Columns are fixed by hardware (pin-programmable).

(Example)

Text home address : 0000H
Text area : 0020H
: 32 Columns
: 4 Lines

0000H	0001H		001EH	001FH
0020H	0021H		003EH	002FH
0040H	0041H		005EH	005FH
0060H	0061H		007EH	007FH

(2) Set Graphic Home Address

The starting address of the external display RAM used for graphic display is defined by this command.

The graphic home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

GH		GH+GL
GH+GA		GH+GA+CL
(GH+GA)+GA		GH+2GA+CL
(GH+2GA)+GA		GH+3GA+CL
GH+(n-1)GA		GH+(n-1)GA+CL

GH: Graphic home address

GA: Graphic area number (columns)

CL: Columns are fixed by hardware (pin-programmable).

(Example)

Graphic home address	: 0000H
Graphic area	: 0020H
	: 32 Columns
	: 2 Lines

(3)Set Text Area

(Example)

Text area 0014H

Set 32 columns, 4 Lines

Page

003C	003D		004F	0050		005B
------	------	-------	------	------	-------	------

(4)Set Graphic Area

The display columns are defined by the hardware setting. This command can be used to adjust the columns of the graphic display.

(Example)

LCD size 20 columns, 2lines
 Graphic home address : 0000H
 Graphic are : 0014H

Set 32 columns, 2 Lines

0000	0001		0013	0014		001F
0014	0015		0027	0028		0033
0028	0029		003B	003C		0047
003C	003D		004F	0050		005B
0050	0051		0063	0064		006F
0064	0065		0077	0078		0083
0078	0079		008B	008C		0097
008C	008D		009F	00A0		00AB
00A0	00A1		00B3	00B4		00BF
00B4	00B5		00C7	00C8		00D3
00C8	00C9		00DB	00DC		00E7
00DC	00DD		00EF	00F0		00FD
00F0	00F1		0103	0104		011F

0104	0105		0127	0128		0123
0128	0129		013B	0013C		00147
013C	013D		014F	0150		015B



If the graphic area setting is set to match the desired number of columns on the LCD, the addressing scheme will be automatically modified so that the start address of each line equals the end address of the previous line +1.

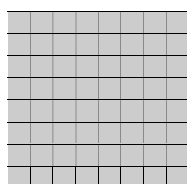
Mode set

CODE	FUNCTION	OPERAND
1000 × 000	OR Mode	—
1000 × 001	EXOR Mode	—
1000 × 011	AND Mode	—
1000 × 100	TEXT ATTRIBUTE Mode	—
10000 × ×	Internal Character Generator Mode	—
10001 × ×	External Character Generator Mode	—

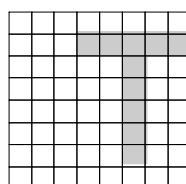
X: invalid

The display mode is defined by this command. The display mode does not change until the next command is sent. The logical OR, EXOR, AND of text or graphic display can be displayed. In Internal Character Generator mode, character codes 00H to 7FH are assigned to the built-in character generator ROM. The character codes 80H to FFH are automatically assigned to the external character generator RAM.

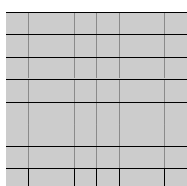
(Example)



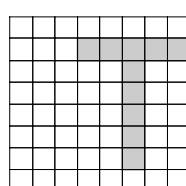
GRAPHIC



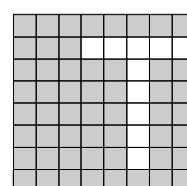
TEXT



“OR”



“AND”



“TXOR”

(Note) Attribute functions can only be applied to text display, since the attribute data is placed in the graphic RAM area.

Attribute function

The attribute operations are Reverse display, Character blink and Inhibit. The attribute data is written into the graphic area which was defined by the Set Control Word command. Only text display is possible in Attribute Function mode; graphic display is automatically disabled. However, the Display Mode command must be used to turn both Text and Graphic on in order for the Attribute function to be available.

The attribute data for each character in the text area is written to the same address in the graphic area. The Attribute function is defined as follows.

Attribute RAM 1byte

×	×	×	×	d3	d2	d1	d0
---	---	---	---	----	----	----	----

d3	d2	d1	d0	FUNCTION
0	0	0	0	Normal display
0	1	0	1	Reverse display
0	0	1	1	Inhibit display
1	0	0	0	Blink of normal display
1	1	0	1	Blink of reverse display
1	0	1	1	Blink of inhibit display

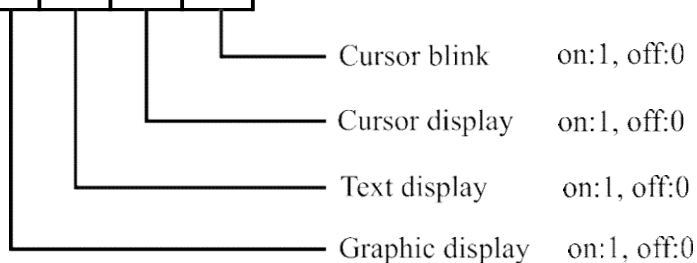
X: invalid

Display mode

CODE	FUNCTION	OPERAND
10010000	Display off	—
1001 × × 10	Cursor on, blink off	—
1001 × × 11	Cursor on, blink on	—
100101 × ×	Text on, graphic off	—
100110 × ×	Text off, graphic on	—
100111 × ×	Text on, graphic on	—

X: invalid

1	0	0	1	D3	D2	D1	D0
---	---	---	---	----	----	----	----



(Note) It is necessary to turn on “Text display” and “Graphic display” in the following cases.

a) Combination of text/graphic display

b) Attribute function

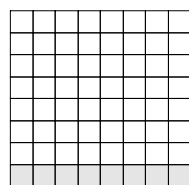
Cursor pattern select

CODE	FUNCTION	OPERAND
------	----------	---------

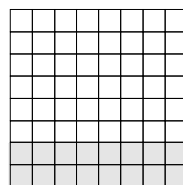
10100000	1-line cursor	—
10100001	2-line cursor	—
10100010	3-line cursor	—
10100011	4-line cursor	—
10100100	5-line cursor	—
10100101	6-line cursor	—
10100110	7-line cursor	—
10100111	8-line cursor	—

When cursor display is ON, this command selects the cursor pattern in the range 1 line to 8 lines.

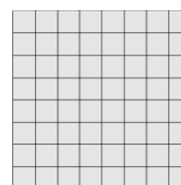
The cursor address is defined by the Cursor Pointer Set command.



1-line cursor



2-line cursor



8-line cursor

Data Auto Read/Write

CODE	HEX.	FUNCTION	OPERAND
10110000	B0H	Set Data Auto Write	—
10110001	B1H	Set Data Auto Read	—
10110010	B2H	Auto Reset	—

The command is convenient for sending a full screen of data from the external display RAM. After setting Auto mode, a Data Write (or Read) command is need not be sent between each datum. A Data Auto Write (or Read) command must be sent after a Set Address Pointer command. After this command, the address pointer is automatically incremented by 1 after each datum. In Auto mode, the T6963C cannot accept any other commands.

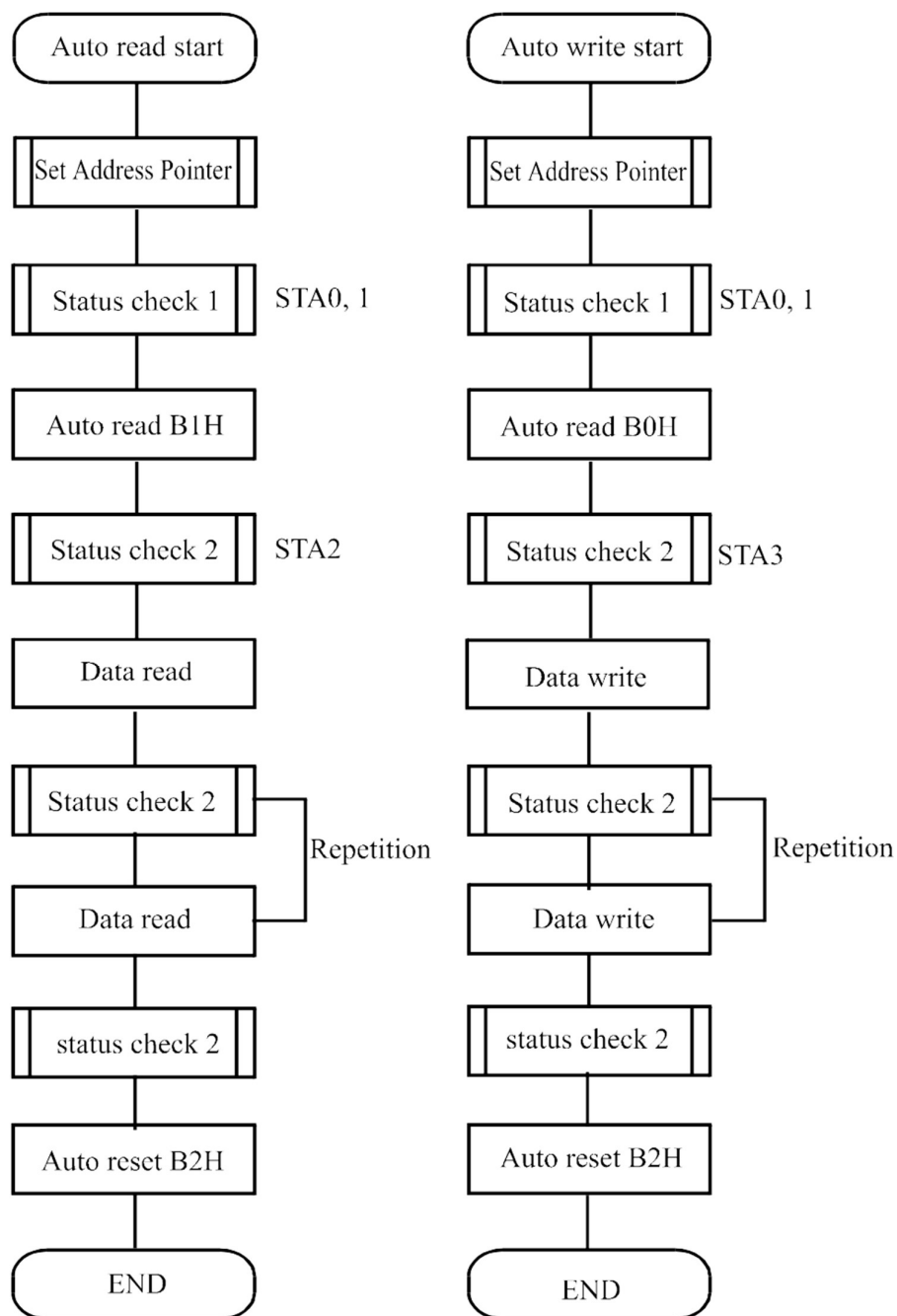
The Auto Reset command must be sent to the T69963C after all data has been sent, to clear Auto mode.

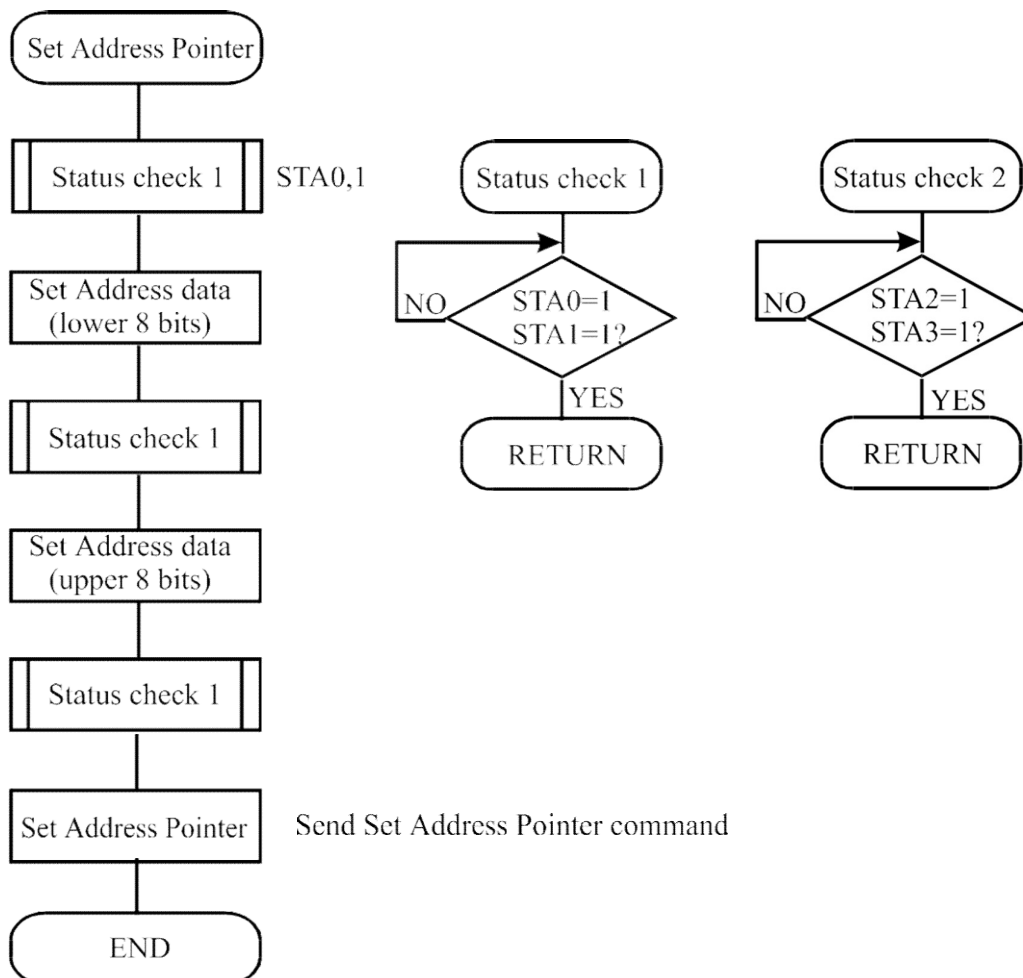
(Note) A Status check for Auto mode

(STA2, STA3 should be checked between sending of each datum. Auto Reset should be performed after checking STA3=1 (STA2=1.) Refer to the following flowchart.

a)Auto Read mode

b)Auto Write mode





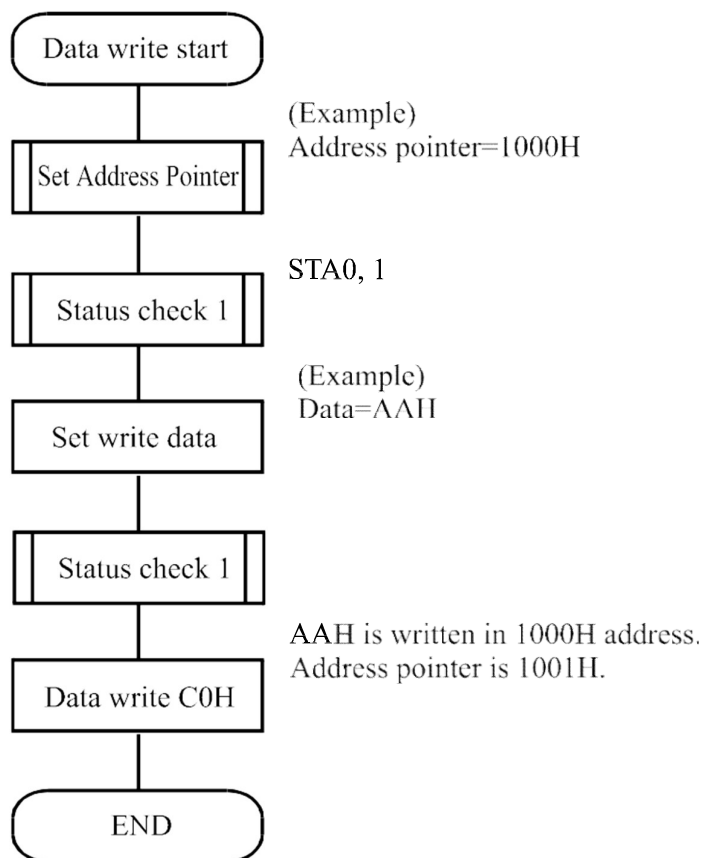
Date Read/Write

CODE	HEX.	FUNCTION	OPERAND
11000000	C0H	Data Write and Increment ADP	Data
11000001	C1H	Data Read and Increment ADP	—
11000010	C2H	Data Write and Decrement ADP	Data
11000011	C3H	Data Read and Decrement ADP	—
11000100	C4H	Data Write and Non-variable ADP	Data
11000101	C5H	Data Read and Non-variable ADP	—

This command is used for writing data from the MPU to external display RAM, and reading data from external display RAM to the MPU. Data Write/Data Read should be executed after setting address using Set Address Pointer command. The address pointer can be automatically incremented or decremented using this command.

(Note) This command is necessary for each 1-byte datum.

Refer to the following flowchart.



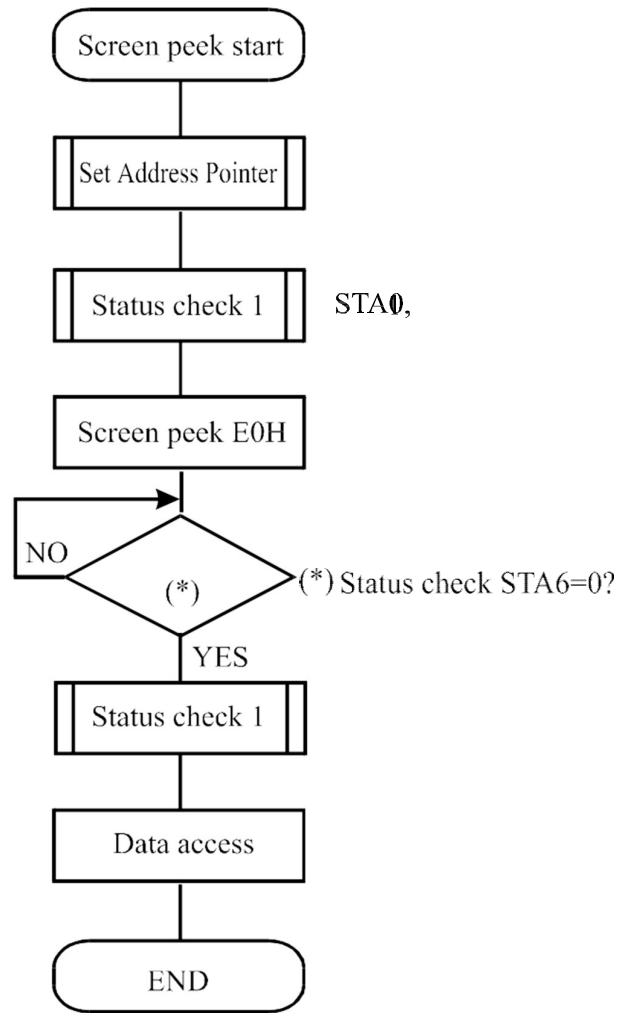
Screen Peek

CODE	HEX.	FUNCTION	OPERAND
11100000	E0H	Screen Peek	— e

This command is used to transfer 1 byte of displayed data to the data stack; this byte can then be read from the MPU by data access. The logical combination of text and graphic display data on the LCD screen can be read by this command.

The status (STA6) should be checked just after the Screen Peek command. If the address determined by the Set Address Pointer command is not in the graphic area, this commands is ignored and a status flag (STA6) is set.

Refer to the following flowchart.



Screen Copy

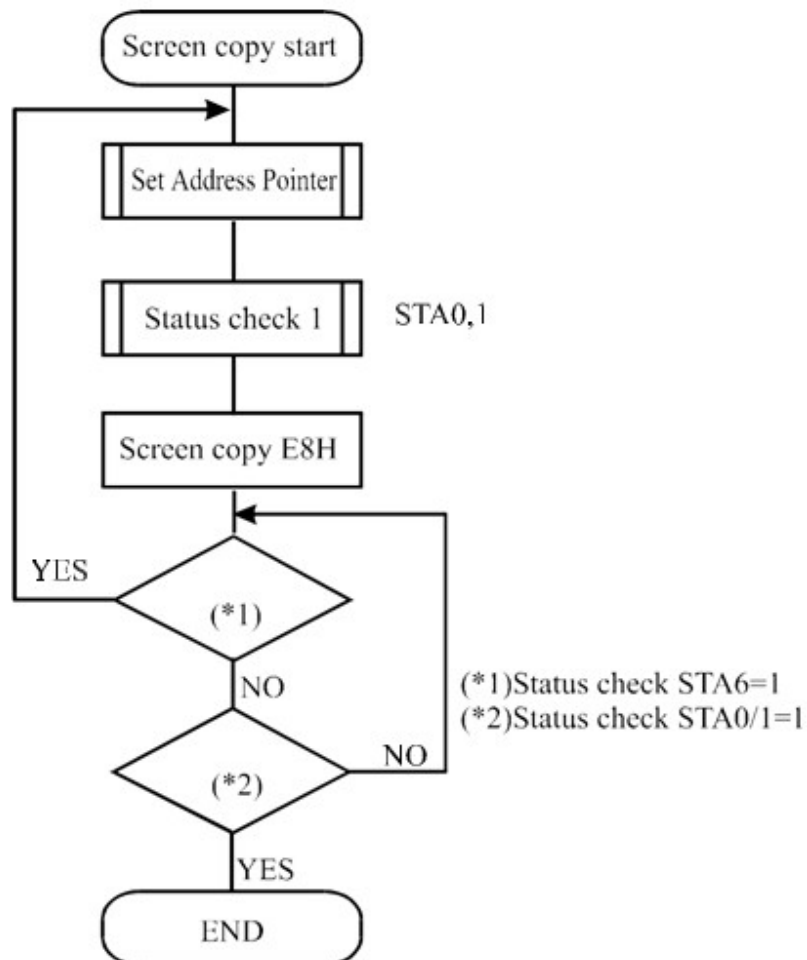
CODE	HEX.	FUNCTION	OPERAND
11101000	E8H	Screen Copy	—

This command copies a single raster line of data to the graphic area.

The start point must be set using the Set Address Pointer command.

(Note 1) If the attribute function is being used, this command is not available. (With Attribute data is graphic area data.)

Refer to the following flowchart.



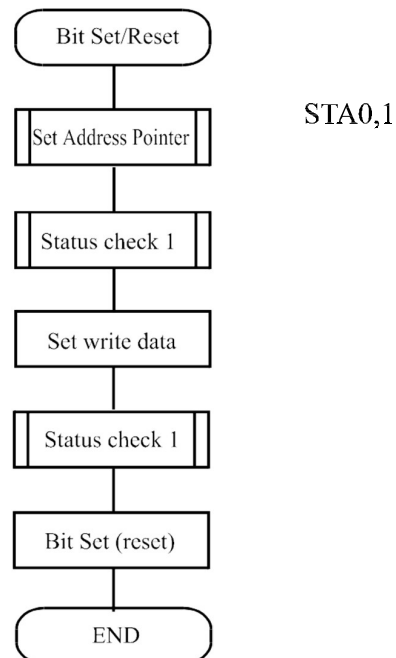
· Bit Set/Reset

CODE	FUNCTION	OPERAND
11110 × × ×	Bit Reset	—
11111 × × ×	Bit Set	—
1111 × 000	Bit 0 (LSB)	—
1111 × 001	Bit 1	—
1111 × 010	Bit 2	—
1111 × 011	Bit 3	—
1111 × 100	Bit 4	—
1111 × 101	Bit 5	—
1111 × 110	Bit 6	—
1111 × 111	Bit 7 (MSB)	—

X: invalid

This command use to set or reset a bit of the byte specified by the address pointer.

Only one bit can be set/reset at a time. Refer to the following flowchart.

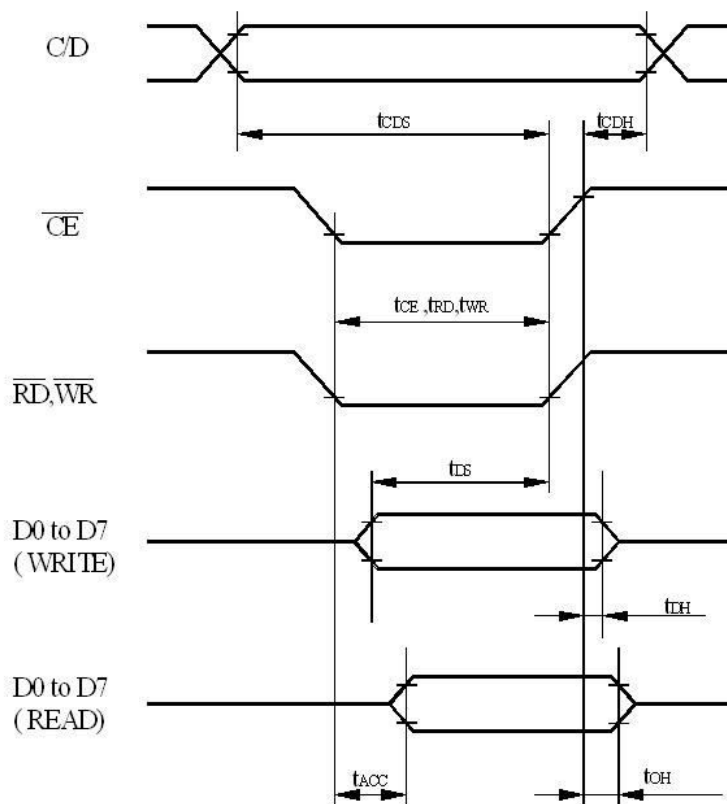


4.7 CHARACTER ROM MAP

Upper 4 bit Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH
LLLL		0	1	2	3	4	5	6
LLLH	7	8	9	A	B	C	D	E
LLHL	F	G	H	I	J	K	L	M
LLHH	N	O	P	Q	R	S	T	U
LHLL	V	W	X	Y	Z	[	\	]
LHLH	^	_	`	a	b	c	d	e
LHHL	f	g	h	i	j	k	l	m
LHHH	n	o	p	q	r	s	t	u
HLLL	v	w	x	y	z	{		}
HLLH	~	!	@	#	\$	%	&	'
HLHL	(	)	*	+	=	<	>	?
HLHH	~	!	@	#	\$	%	&	'
HHLL	(	)	*	+	=	<	>	?
HHLH	~	!	@	#	\$	%	&	'
HHHL	(	)	*	+	=	<	>	?
HHHH	~	!	@	#	\$	%	&	'

4.8 TIMING CHARACTERISTICS

Item	Symbol	Min	Typ	Max	Unit
C/D Set-up Time	tCDS	100	—	—	ns
C/D Hold Time	tCDH	10	—	—	ns
CE,RD,WR Pulse Width	tCDS,tRD,tWR	80	—	—	ns
Data Set-up Time	tDS	80	—	—	ns
Data Hold Time	tDH	40	—	—	ns
Access Time	tACC	—	—	150	ns
Output Hold Time	tOH	10	—	50	ns



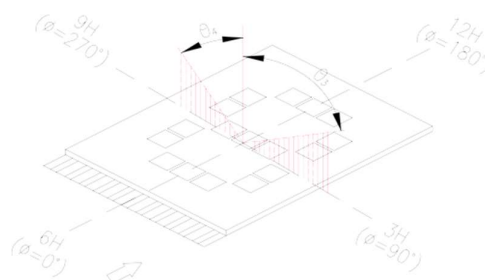
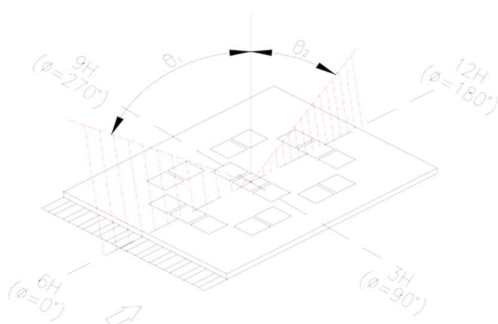
5 OPTICAL SPECIFICATION

Ta = 25 °C

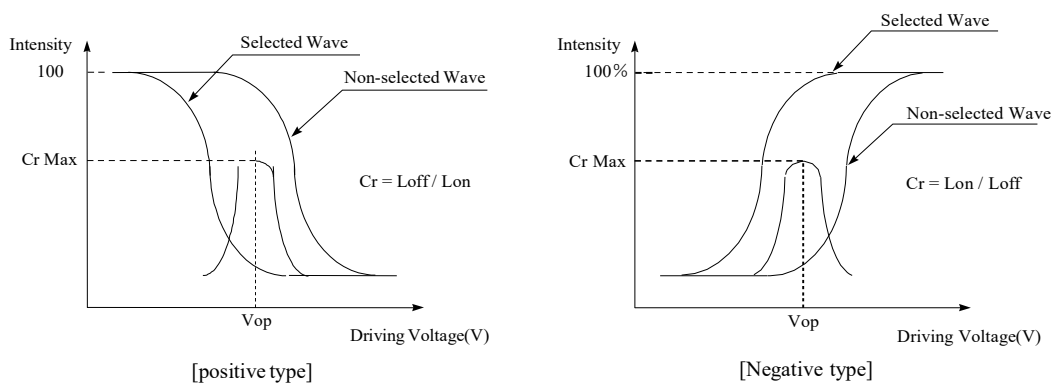
Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Viewing Angle in STN	θ1	CR≥2	-	60	-	deg	1
	θ2	CR≥2	-	30	-	deg	1
	θ3	CR≥2	-	30	-	deg	2
	θ4	CR≥2	-	30	-	deg	2
Viewing Angle in FSTN	θ1	CR≥2	-	120	-	deg	1
	θ2	CR≥2	-	10	-	deg	1
	θ3	CR≥2	-	45	-	deg	2
	θ4	CR≥2	-	45	-	deg	2
Contrast Ratio	CR	Ta = 25 °C	-	5	-	-	3
Response Time	Tr	Ta = 25 °C	-	200	300	ms	4
	Tf	Ta = 25 °C	-	150	250		
Driving Method	Duty	1/64					
Viewing Direction	6 O'CLOCK						

Note 1: definition of viewing angle θ1 & θ2

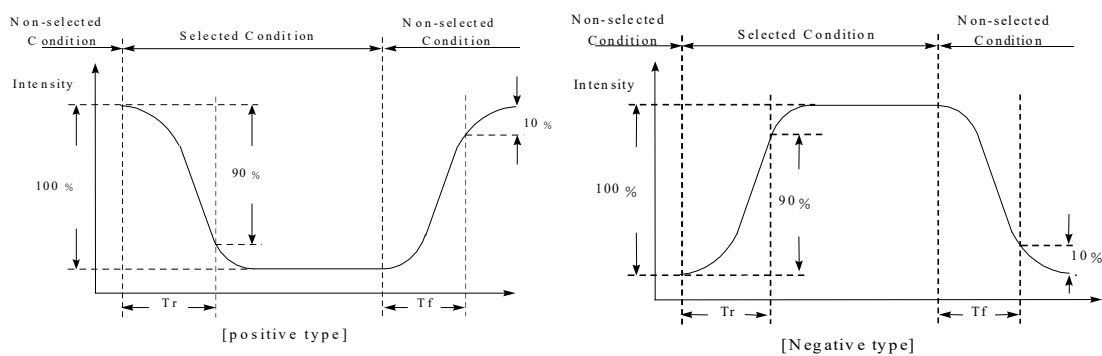
Note 2: definition of viewing angle θ3 & θ4



Note 3: definition of contrast ratio (CR)



Note 4: definition of response time



6 TOUCH SCREEN SPECIFICATION

No Touch option available for this module

7 BACKLIGHT SPECIFICATION

7.1 LED BACKLIGHT CHARACTERISTICS

7.1.1 WHITE STANDARD LED BACKLIGHT CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Current	I	V = 3.5V	-	80	-	mA
Forward Voltage	V _F	I _F = 80mA	-	3.5	3.6	V
Reverse Voltage	V _R		-	-	8	V
Luminous Intensity	I _V	I _F = 80mA	-	80	-	cd/m ²
Life time		I _F = 80mA	-	50K	-	hrs
Colour	WHITE					

7.1.2 WHITE EDGE STANDARD LED BACKLIGHT CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Current	I	V = 3.5V	-	100	-	mA
Forward Voltage	V _F	I _F = 80mA	-	3.5	3.6	V
Reverse Voltage	V _R		-	-	8	V
Luminous Intensity	I _V	I _F = 100mA	-	80	-	cd/m ²
Life time		I _F = 100mA	-	100K	-	hrs
Colour	WHITE					

7.1.3 WHITE EDGE HIGH BRIGHT LED BACKLIGHT CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Current	I	V = 3.5V	-	100	-	mA
Forward Voltage	V _F	I _F = 80mA	-	3.5	3.6	V
Reverse Voltage	V _R		-	-	8	V
Luminous Intensity	I _V	I _F = 100mA	-	400	-	cd/m ²
Life time		I _F = 100mA	-	50K	-	hrs
Colour	WHITE					

7.1.4 YELLOW GREEN STANDARD LED BACKLIGHT CHARACTERISTICS

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Current	I	V = 4.2V	-	660	1320	mA
Forward Voltage	V _F	I _F = 660mA	-	4,2	4,6	V
Reverse Voltage	V _R		-	-	8	V
Luminous Intensity	I _V	I _F = 660mA	-	160	-	cd/m ²
LED Peak Emission Wavelength	λ _p	I _F = 660mA	-	570	-	nm
Life time		I _F = 660mA	-	100K	-	hrs
Colour	YELLOW GREEN					

8 QUALITY ASSURANCE SPECIFICATION

8.1 CONFORMITY

The performance, function and reliability of the shipped products conform to the Product Specification.

8.2 DELIVERY ASSURANCE

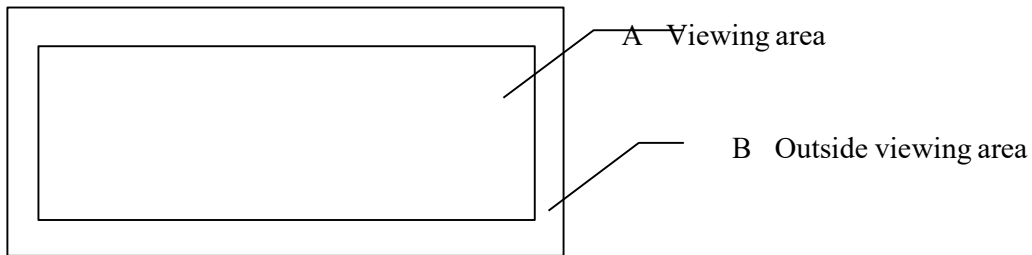
8.2.1 Delivery inspection standards.

- MIL-STD-105E, general inspection level II, single sampling level;
- IPC-AA610 rev. C, class 2 electronic assemblies standard

The quality assurance levels are shown below:

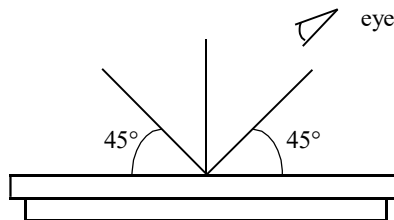
Rank	Item Inspected	Defect type	AQL	Remark
Critical defect	Display	Non display	0.65%	Display malfunction
		Over current		
		Missing segment		
		Wrong viewing direction		
		Backlight OFF		
	Dimension	PCB and bezel out of specification	0.65%	Assembly failure
Major defect	Display	Incorrect operating	1.0%	
	Backlight	Flashing, dust		
		Wrong colour		
Minor defect	LCD	Black and white spot	2.5%	Appearance defect
		Black and white lines		
		Polariser scratch		
		Bubbles in polariser		
		Segment deformation, pin hole		
		Colour uniformity		
		Glass chip		
	COB	Wire bond pad exposed		
		Insufficient covering with resin (wire bond line exposed)		
		Bubble, dust on COB		
	PCB	Dust, solder ball on PCB		
		Pad scratch		
Total			2.5%	

8.2.2 Zone definition



8.2.3 Visual inspection

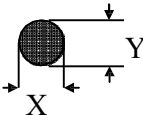
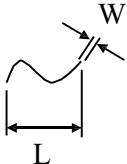
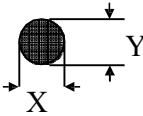
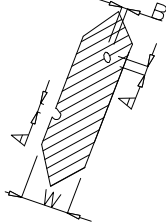
- Inspect under 2x20W or 40W fluorescent lamp (approximately 3000 lux) leaving 25 to 30 cm between the module and the lamp and 30 cm between the module and the eye (measuring position).
- Appearance is inspected at the best contrast voltage (best contrast is adjusted considering clearness and crosstalk on screen).
- Inspect the module at 45° right and left, top and bottom.
- Use the optimum viewing angle during the contrast inspection.

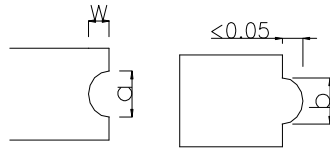
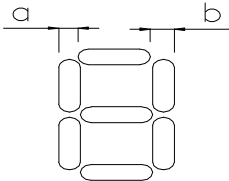
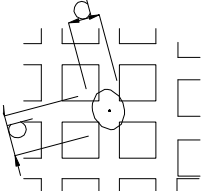
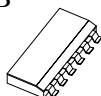


8.2.4 Standard of appearance inspection

Units: mm

Units: mm

No	Item	Criteria																																			
1	Black spot, white spot, dust	Round type: as per following drawing $\varnothing = (X+Y)/2$  <table><tr><th colspan="3">Acceptable quantity</th></tr><tr><th>Size</th><th>Zone A</th><th>Zone B</th></tr><tr><td>$\varnothing < 0.1$</td><td>Any number</td><td rowspan="4">Any number</td></tr><tr><td>$0.1 < \varnothing < 0.2$</td><td>6</td></tr><tr><td>$0.2 < \varnothing < 0.3$</td><td>2</td></tr><tr><td>$0.3 < \varnothing$</td><td>0</td></tr></table> Line type: as per following drawing  <table><tr><th colspan="4">Acceptable quantity</th></tr><tr><th>Length</th><th>Width</th><th>Zone A</th><th>Zone B</th></tr><tr><td>--</td><td>$W \leq 0.02$</td><td>Any number</td><td rowspan="4">Any number</td></tr><tr><td>$L \leq 3.0$</td><td>$0.02 < W \leq 0.03$</td><td rowspan="2">2</td></tr><tr><td>$L \leq 2.5$</td><td>$0.03 < W \leq 0.05$</td></tr><tr><td>--</td><td>$0.05 < W$</td><td>As round type</td></tr></table> Total acceptable quantity: 3	Acceptable quantity			Size	Zone A	Zone B	$\varnothing < 0.1$	Any number	Any number	$0.1 < \varnothing < 0.2$	6	$0.2 < \varnothing < 0.3$	2	$0.3 < \varnothing$	0	Acceptable quantity				Length	Width	Zone A	Zone B	--	$W \leq 0.02$	Any number	Any number	$L \leq 3.0$	$0.02 < W \leq 0.03$	2	$L \leq 2.5$	$0.03 < W \leq 0.05$	--	$0.05 < W$	As round type
Acceptable quantity																																					
Size	Zone A	Zone B																																			
$\varnothing < 0.1$	Any number	Any number																																			
$0.1 < \varnothing < 0.2$	6																																				
$0.2 < \varnothing < 0.3$	2																																				
$0.3 < \varnothing$	0																																				
Acceptable quantity																																					
Length	Width	Zone A	Zone B																																		
--	$W \leq 0.02$	Any number	Any number																																		
$L \leq 3.0$	$0.02 < W \leq 0.03$	2																																			
$L \leq 2.5$	$0.03 < W \leq 0.05$																																				
--	$0.05 < W$	As round type																																			
2	Polariser scratch	Scratch on protective film is permitted Scratch on polariser: same as No. 1																																			
3	Polariser bubble	$\varnothing = (X+Y)/2$  <table><tr><th colspan="3">Acceptable quantity</th></tr><tr><th>Size</th><th>Zone A</th><th>Zone B</th></tr><tr><td>$\varnothing < 0.3$</td><td>Any number</td><td rowspan="4">Any number</td></tr><tr><td>$0.3 < \varnothing < 1.0$</td><td>3</td></tr><tr><td>$1.0 < \varnothing < 1.5$</td><td>1</td></tr><tr><td>$1.5 < \varnothing$</td><td>0</td></tr></table> Total acceptable quantity: 4	Acceptable quantity			Size	Zone A	Zone B	$\varnothing < 0.3$	Any number	Any number	$0.3 < \varnothing < 1.0$	3	$1.0 < \varnothing < 1.5$	1	$1.5 < \varnothing$	0																				
Acceptable quantity																																					
Size	Zone A	Zone B																																			
$\varnothing < 0.3$	Any number	Any number																																			
$0.3 < \varnothing < 1.0$	3																																				
$1.0 < \varnothing < 1.5$	1																																				
$1.5 < \varnothing$	0																																				
4	Segment deformation	1.a. Pin hole on segmented display W: segment width $\varnothing = (A+B)/2$  <table><tr><th colspan="2">Acceptable quantity</th></tr><tr><th>Width</th><th>$\varnothing$</th></tr><tr><td>$W \leq 0.4$</td><td>$\varnothing \leq 0.2$ and $\varnothing \leq 1/2 W$</td></tr><tr><td>$W > 0.4$</td><td>$\varnothing \leq 0.25$ and $\varnothing \leq 1/3 W$</td></tr></table> Total acceptable quantity: 1 defect per segment Pin holes with $\varnothing$ under 0.10 mm are acceptable	Acceptable quantity		Width	$\varnothing$	$W \leq 0.4$	$\varnothing \leq 0.2$ and $\varnothing \leq 1/2 W$	$W > 0.4$	$\varnothing \leq 0.25$ and $\varnothing \leq 1/3 W$																											
Acceptable quantity																																					
Width	$\varnothing$																																				
$W \leq 0.4$	$\varnothing \leq 0.2$ and $\varnothing \leq 1/2 W$																																				
$W > 0.4$	$\varnothing \leq 0.25$ and $\varnothing \leq 1/3 W$																																				

No	Item	Criteria																												
4	Segment deformation	1b. Pin hole on dot matrix display  <table><tr><th colspan="2">Acceptable quantity</th></tr><tr><th>Size</th><th></th></tr><tr><td>a,b<0.1</td><td>Any number</td></tr><tr><td>(a+b)/2≤0.1</td><td>Any number</td></tr><tr><td>0.5<Ø<1.0</td><td>3</td></tr></table> Total acceptable quantity: 7 2. Segments / dots with different width  <table><tr><th colspan="2">Acceptable</th></tr><tr><td>a≥b</td><td>a/b≤4/3</td></tr><tr><td>a<b</td><td>a/b>4/3</td></tr></table> 3. Alignment layer defect Ø = (a+b)/2  <table><tr><th colspan="2">Acceptable quantity</th></tr><tr><th>Size</th><th></th></tr><tr><td>Ø≤0.4</td><td>Any number</td></tr><tr><td>0.4<Ø≤1.0</td><td>5</td></tr><tr><td>1.0<Ø≤1.5</td><td>3</td></tr><tr><td>1.5<Ø≤2.0</td><td>2</td></tr></table> Total acceptable quantity: 7	Acceptable quantity		Size		a,b<0.1	Any number	(a+b)/2≤0.1	Any number	0.5<Ø<1.0	3	Acceptable		a≥b	a/b≤4/3	a<b	a/b>4/3	Acceptable quantity		Size		Ø≤0.4	Any number	0.4<Ø≤1.0	5	1.0<Ø≤1.5	3	1.5<Ø≤2.0	2
Acceptable quantity																														
Size																														
a,b<0.1	Any number																													
(a+b)/2≤0.1	Any number																													
0.5<Ø<1.0	3																													
Acceptable																														
a≥b	a/b≤4/3																													
a<b	a/b>4/3																													
Acceptable quantity																														
Size																														
Ø≤0.4	Any number																													
0.4<Ø≤1.0	5																													
1.0<Ø≤1.5	3																													
1.5<Ø≤2.0	2																													
5	Colour uniformity	Level of sample for approval set as limit sample																												
6	Backlight	The backlight colour should correspond to the product specification Flashing and or unlit backlight is not allowed Dust larger than 0.25 mm is not allowed																												
7	COB	Exposed wire bond pad is not allowed Insufficient covering with resin is not allowed (wire bond line exposed) Dust or bubble on the resin are not allowed																												
8	PCB 	No unmelted solder paste should be present on PCB Cold solder joints, missing solder connections, or oxidation are not allowed No residue or solder balls on PCB are allowed Short circuits on components are not allowed																												

9 RELIABILITY SPECIFICATION

Test Item	Test Condition	Description
High Temperature Operation	50°C or 70°C 200hrs	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.
Low Temperature Operation	0°C or -20°C 200hrs	Endurance test applying the electric stress under low temperature for a long time.
High Temperature Storage	70°C or 80°C 200hrs	Endurance test applying the high storage temperature for a long time.
Low Temperature Storage	-20°C or -30°C 200hrs	Endurance test applying the high storage temperature for a long time.
High Temperature & High Humidity Storage	80°C,90%RH 96hrs	Endurance test applying the high temperature and high humidity storage for a long time.
Thermal Shock Test	$\xleftarrow{30^{\circ}\text{C}} \quad 25^{\circ}\text{C} \quad \xrightarrow{80^{\circ}\text{C}}$ 30min 5min 30min For 10 cycles	Endurance test applying the low and high temperature cycle. Burn In Test.
Vibration	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs	Endurance test applying the vibration during transportation and using.
ESD	VS=800V,RS=1.5kΩ CS=100pF	Endurance test applying the electric stress to the terminal.
Shock Test	50G Half sign wave 11 msdc 3 times of each direction	Constructional and mechanical endurance test applying the shock during transportation.

10 HANDLING PRECAUTIONS

Safety

If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes.

If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

Mounting and Design

Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface.

When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air very clean.

Design the system so that no input signal is given unless the power supply voltage is applied.

Caution during LCD cleaning

Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotrifluoroethane.

Do not wipe the display surface with dry or hard materials that will damage the polariser surface.

Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

Caution against static charge

As the display uses C-MOS LSI drivers, connect any unused input terminal to VDD or VSS. Do not input any signals before power is turned on.

Also, ground your body, work/assembly table and assembly equipment to protect against static electricity.

Packaging

Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height.

To prevent displays from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.

Caution during operation

It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life.

Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation.

Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged.

If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once.

Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

Storage

Store the display in a dark place where the temperature is $25^{\circ}\text{C} \pm 10^{\circ}\text{C}$ and the humidity below 50%RH.

Store the display in a clean environment, free from dust, organic solvents and corrosive gases.

Do not crash, shake or jolt the display (including accessories).